Points missed: _____  
Student's Name: ___________________________________________

Total score: _____/100 points

East Tennessee State University – Department of Computer and Information Sciences
CSCI 2150 (Tarnoff) – Computer Organization
TEST 1 for Fall Semester, 2008

Read this before starting!

- The total possible score for this test is 100 points.
- This test is closed book and closed notes
- Please turn off all cell phones & pagers during the test.
- You may NOT use a calculator. Leave all numeric answers in the form of a formula.
- You may use one sheet of scrap paper that you must turn in with your test.
- Please draw a box around your answers. This is to aid the grader. Failure to do so might result in no credit for answer. Example:

\[ 32F_{16} \]

- 1 point will be deducted per answer for missing or incorrect units when required. No assumptions will be made for hexadecimal versus decimal versus binary, so you should always include the base in your answer.
- If you perform work on the back of a page in this test, indicate that you have done so in case the need arises for partial credit to be determined.
- Statement regarding academic misconduct from Section 5.7 of the East Tennessee State University Faculty Handbook, June 1, 2001:

"Academic misconduct will be subject to disciplinary action. Any act of dishonesty in academic work constitutes academic misconduct. This includes plagiarism, the changing of falsifying of any academic documents or materials, cheating, and the giving or receiving of unauthorized aid in tests, examinations, or other assigned school work. Penalties for academic misconduct will vary with the seriousness of the offense and may include, but are not limited to: a grade of 'F' on the work in question, a grade of 'F' of the course, reprimand, probation, suspension, and expulsion. For a second academic offense the penalty is permanent expulsion."

Basic Rules of Boolean Algebra

<table>
<thead>
<tr>
<th></th>
<th>OR</th>
<th>AND</th>
<th>XOR</th>
</tr>
</thead>
<tbody>
<tr>
<td>Combined w/0</td>
<td>( A + 0 = A )</td>
<td>( A \cdot 0 = 0 )</td>
<td>( A \oplus 0 = A )</td>
</tr>
<tr>
<td>Combined w/1</td>
<td>( A + 1 = 1 )</td>
<td>( A \cdot 1 = A )</td>
<td>( A \oplus 1 = \overline{A} )</td>
</tr>
<tr>
<td>Combined w/self</td>
<td>( A + A = A )</td>
<td>( A \cdot A = A )</td>
<td>( A \oplus A = 0 )</td>
</tr>
<tr>
<td>Combined w/inverse</td>
<td>( A + \overline{A} = 1 )</td>
<td>( A \cdot \overline{A} = 0 )</td>
<td>( A \oplus \overline{A} = 1 )</td>
</tr>
<tr>
<td>Other rules</td>
<td>( A + A \cdot B = A )</td>
<td>( A + \overline{A} \cdot B = A + B )</td>
<td>( (A + B) \cdot (A + C) = A + B \cdot C )</td>
</tr>
<tr>
<td>DeMorgan's Th.</td>
<td>( A \cdot B = \overline{A} + \overline{B} )</td>
<td>( A + B = \overline{A} \cdot \overline{B} )</td>
<td></td>
</tr>
</tbody>
</table>
Short-ish Answer (2 points each unless otherwise noted)

1. For a system to reliably capture the frequency range from 250 Hz to 500 Hz, the system's sampling rate must be greater than __________.
   a.) 250 samples/sec.  b.) 500 samples/sec.  c.) 750 samples/sec.  d.) 1000 samples/sec.

2. What is the frequency of the signal shown to the right?  
   (Note: 1 millisecond = $1 \times 10^{-3}$ seconds)

   ![Signal Diagram]

3. The duty cycle for the previous problem is:
   a.) 25%  b.) 33%  c.) 40%  d.) 50%  e.) 66%  f.) 75%  g.) 100%

4. True or False: The frequency of a periodic signal can be calculated using only the duty cycle.

5. What is the most negative value that can be stored using 10-bit 2's complement representation?
   a.) $-2^{10}$  b.) $-(2^{10} - 1)$  c.) $-2^9$  d.) $-(2^9 - 1)$  e.) $-2^8$  f.) $-(2^8 - 1)$

6. What is the minimum number of bits needed to represent 999$_{10}$ in unsigned binary representation?
   a.) 8  b.) 9  c.) 10  d.) 11  e.) 12  f.) 13

7. Which signed binary representation works for addition of both positive and negative values?
   a.) signed magnitude  b.) 2's complement  c.) neither

8. How many patterns of ones and zeros does a 6-bit Gray code sequence consists of?

9. The IEEE-754 32-bit floating-point value 01000000100101000000000000000000 is _______.  
   (3 points)
   a.) less than zero  b.) between zero and one  c.) greater than one

10. Write the complete truth table for a 2-input XOR gate. (3 points)

   | A | B | X |
---|---|---|---|
 | 0 | 0 | 0 |
 | 0 | 1 | 1 |
 | 1 | 0 | 1 |
 | 1 | 1 | 0 |

11. True or False: If the addition above is considered 8-bit 2’s complement, an overflow has occurred.

12. True or False: If the addition above is considered 8-bit unsigned, an overflow has occurred.
13. In the boolean expression below, circle the single operation that would be performed first.

\[ A + (B + C) \cdot D \]

14. Multiply the 16-bit value 00001110111010002 by 8. Leave your answer in 16-bit binary. (Hint: Remember the shortcut!)

15. Convert 110100001101111011011112 to hexadecimal.

16. Convert the unsigned binary value 0101002 to its corresponding 6-bit binary Gray code. (3 points)

Medium-ish Answer (4 points each unless otherwise noted)

17. Convert the 32-bit IEEE 754 floating-point number 0011111101101100000000000000000000000000000 to its binary exponential format, e.g., \(-1.001010 \times 2^{-12}\), (which, by the way, is not the answer).

18. Convert 1011.1012 to decimal. (You may leave your answer in expanded form if you wish.)

19. Draw the circuit exactly as it is represented by the Boolean expression \( \bar{A} \cdot B + B \cdot \bar{C} \).

20. Prove that \( A \oplus \bar{A} = 1 \). (Remember that \( \oplus \) is the XOR or exclusive-OR) For full credit, please show all steps in detail.
21. In the space to the right, create the truth table for the circuit shown below. (6 points)

A | B | C
---|---|---
A | B | X

22. Write the Boolean expression for the circuit shown in the previous problem. *Do not simplify!*

23. Use DeMorgan's Theorem to distribute the inverse of the expression \( A \cdot B + \overline{C} \) all of the way to the individual input terms. Be careful of the AND and OR precedence. It might help to see the expression as a logic circuit. *Do not simplify!*

24. Assume that an 8-bit binary number is used to represent an analog value in the range from -8 to 119. Convert all four of the following binary values to their analog equivalent, i.e., what analog value does each of these binary values represent? You may leave your answer in the form of a fraction in some cases if you wish. (5 points)

a.) \( 00000000_2 \)

b.) \( 00000001_2 \)

c.) \( 00001010_2 \)

d.) \( 11111111_2 \)
25. Mark each Boolean expression as true or false depending on whether the right and left sides of the equal sign are equivalent. Show all of your work to receive partial credit for incorrect answers. (3 points each)

   a.) \( A \cdot B \cdot (\overline{A} + \overline{B}) = 0 \)
   
   Answer: _____________

   b.) \( A + (A + \overline{B}) + \overline{A} \cdot B \cdot C = A + \overline{B} \)
   
   Answer: _____________

   c.) \( (A + B \cdot C) + A \cdot \overline{B} = A \cdot \overline{B} \)
   
   Answer: _____________

26. Fill in the blank cells of the table below with the correct numeric format. For cells representing binary values, only 8-bit values are allowed! If a value for a cell is invalid or cannot be represented in that format, write "X". (7 points per row)

<table>
<thead>
<tr>
<th>Decimal</th>
<th>2's complement binary</th>
<th>Signed magnitude binary</th>
<th>Unsigned binary</th>
<th>Unsigned BCD</th>
</tr>
</thead>
<tbody>
<tr>
<td>-112</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>11000110</td>
<td></td>
<td></td>
<td></td>
<td>11000110</td>
</tr>
</tbody>
</table>