Points missed: _____  Student's Name: ____________________________________________

Total score: _____ /100 points

East Tennessee State University – Department of Computer and Information Sciences
CSCI 2150 (Tarnoff) – Computer Organization
TEST 1 for Spring Semester, 2009

Read this before starting!

- The total possible score for this test is 100 points.
- This test is closed book and closed notes
- Please turn off all cell phones & pagers during the test.
- You may NOT use a calculator. Leave all numeric answers in the form of a formula.
- You may use one sheet of scrap paper that you must turn in with your test.
- Please draw a box around your answers. This is to aid the grader. Failure to do so might result in no credit for answer. Example:

```
32 F 10
```

- 1 point will be deducted per answer for missing or incorrect units when required. No assumptions will be made for hexadecimal versus decimal versus binary, so you should always include the base in your answer.
- If you perform work on the back of a page in this test, indicate that you have done so in case the need arises for partial credit to be determined.
- Statement regarding academic misconduct from Section 5.7 of the East Tennessee State University Faculty Handbook, June 1, 2001:

"Academic misconduct will be subject to disciplinary action. Any act of dishonesty in academic work constitutes academic misconduct. This includes plagiarism, the changing of falsifying of any academic documents or materials, cheating, and the giving or receiving of unauthorized aid in tests, examinations, or other assigned school work. Penalties for academic misconduct will vary with the seriousness of the offense and may include, but are not limited to: a grade of 'F' on the work in question, a grade of 'F' of the course, reprimand, probation, suspension, and expulsion. For a second academic offense the penalty is permanent expulsion."

<table>
<thead>
<tr>
<th>Basic Rules of Boolean Algebra</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>OR</strong></td>
</tr>
<tr>
<td>Combined w/0</td>
</tr>
<tr>
<td>Combined w/1</td>
</tr>
<tr>
<td>Combined w/self</td>
</tr>
<tr>
<td>Combined w/inverse</td>
</tr>
<tr>
<td>Other rules</td>
</tr>
<tr>
<td>DeMorgan's Th.</td>
</tr>
</tbody>
</table>
**Short-ish Answer (2 points each unless otherwise noted)**

1. Which unit of measurement is used to represent the frequency of a periodic waveform?
   a.) Seconds  b.) Percent  c.) Hertz  d.) Seconds per cycle  e.) Cycles

2. What is the frequency of the signal shown to the right?
   (Note: 1 microsecond = $1 \times 10^{-6}$ seconds)
   ![Signal Waveform]

3. The duty cycle for the previous problem is:
   a.) 25%  b.) 33%  c.) 40%  d.) 50%  e.) 66%  f.) 75%  g.) 100%

4. How many patterns of 1's and 0's can be made using 7 bits?

5. You can quickly determine whether a number is positive or negative in the signed representations 2's complement, signed magnitude, and IEEE-754 floating-point by simply looking at the _____.
   a.) most significant bit  b.) least significant bit  c.) number of bits

6. What is the most positive value that can be stored using 10-bit unsigned binary representation?
   a.) $2^{10}$  b.) $2^{10} - 1$  c.) $2^{10}$  d.) $2^{10} - 1$  e.) $2^{9}$  f.) $2^{9} - 1$

7. What is the most negative value that can be stored using 9-bit signed magnitude representation?
   a.) $-2^{10}$  b.) $-(2^{10} - 1)$  c.) $-2^{9}$  d.) $-(2^{9} - 1)$  e.) $-2^{8}$  f.) $-(2^{8} - 1)$

8. What is the minimum number of bits needed to represent 125010 in unsigned binary representation?
   a.) 8  b.) 9  c.) 10  d.) 11  e.) 12  f.) 13

9. The IEEE-754 32-bit floating-point value 11000001001010000000000000000000 is ________.
   a.) positive  b.) negative

10. Write the complete truth table for a 2-input XOR gate. (3 points)
    | A | B | X |
    |---|---|---|
11. Perform the binary addition shown below.

\[
\begin{array}{c}
10110010 \\
+01010001 \\
\end{array}
\]

12. True or false: The above addition works the same regardless of whether the numbers are in 2's complement notation or unsigned binary notation. (1 point)

13. True or false: If the above addition is in 8-bit unsigned binary notation, the result can be contained in 8 bits. (1 point)

14. True or false: If the above addition is in 8-bit 2's complement notation, the result can be contained in 8 bits. (1 point)

15. In the boolean expression below, circle the single operation that would be performed first.

\[ A + B + C \cdot D \cdot E \]

16. Divide the 16-bit value 0000010110100000₂ by 16. Leave your answer in 16-bit binary. (Hint: Remember the shortcut!)

17. Convert 110000011010010101₂ to hexadecimal.

18. Convert the unsigned binary value 001100₂ to its corresponding 6-bit binary Gray code. (3 points)

\[ \text{Medium-ish Answer (4 points each unless otherwise noted)} \]

19. Convert the 32-bit IEEE 754 floating-point number 11000000011010000000000000000000 to its binary exponential format, e.g., \(-1.001010 \times 2^{-12}\), (which, by the way, is not the answer).

20. Convert 0110.011₂ to decimal. (You may leave your answer in expanded form if you wish.)
21. In the space to the right, draw the circuit exactly as it is represented by the following Boolean expression:

\[ A \cdot (B + \overline{C}) \].

22. Prove the rule \( A \oplus 1 = \overline{A} \). (Remember that \( \oplus \) is the XOR or exclusive-OR) For full credit, please show all steps in detail.

23. In the space to the right, create the truth table for the circuit shown below. (6 points)

\[ \begin{array}{ccc}
A & B & C \\
\hline
\end{array} \]

\[ \begin{array}{c}
A \\
B \\
C \\
\end{array} \rightarrow \begin{array}{c}
\text{AND} \\
\text{XOR} \\
\text{AND} \\
\end{array} \rightarrow X \]

24. Write the Boolean expression for the circuit shown in the previous problem. Do not simplify!

25. Use DeMorgan's Theorem to distribute the inverse of the expression \( A + (B \cdot C) + D \) all the way to the individual input terms. Be careful of the AND and OR precedence. It might help to see the expression as a logic circuit. Do not simplify!
26. Assume that an 8-bit binary number is used to represent an analog value in the range from 10 to 20. Convert all four of the following binary values to their analog equivalent, i.e., what analog value does each of these binary values represent? You may leave your answer in the form of a fraction in some cases if you wish. (5 points)

a.) 00000002

b.) 000000012

c.) 000001102

d.) 11111112

**Longer Answers (Points vary per problem)**

27. Mark each Boolean expression as true or false depending on whether the right and left sides of the equal sign are equivalent. Show all of your work to receive partial credit for incorrect answers. (3 points each)

a.) \( A \cdot B + A = 1 \)  
   Answer: ___________

b.) \( A \cdot B \cdot (A + B) = 1 \)  
   Answer: ___________

c.) \( (A \cdot B \cdot C + A \cdot B \cdot C) = 1 \)  
   Answer: ___________
28. Fill in the blank cells of the table below with the correct numeric format. *For cells representing binary values, only 8-bit values are allowed!* If a value for a cell is invalid or cannot be represented in that format, write "X". (7 points per row)

<table>
<thead>
<tr>
<th>Decimal</th>
<th>2's complement binary</th>
<th>Signed magnitude binary</th>
<th>Unsigned binary</th>
<th>Unsigned BCD</th>
</tr>
</thead>
<tbody>
<tr>
<td>-23</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>01010011</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>10001001</td>
<td></td>
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</tbody>
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